

smxARM™

Support for the ARM Processor Family

smxARM is the version of smx designed for the ARM processor family. It has many features to facilitate ARM development, which are discussed below. Features common to all versions of smx are detailed in the main smx brochure.

Processors Supported

smxARM will run on any ARM7 or ARM9 processor. smxBSP and startup code are available for the following processors:

Vendor	Processor	Type
Atmel	AT91x408xx	7TDMI
Atmel	AT91M55800	7TDMI
Atmel	AT91SAM7A1/2/3	7TDMI
Atmel	AT91SAM7S/SE	7TDMI
Atmel	AT91SAM7X	7TDMI
Atmel	AT91RM9200	920T
Atmel	AT91SAM9260	926EJ-S
Atmel	AT91SAM9261	926EJ-S
Atmel	AT91SAM9263	926EJ-S
Cirrus Logic	EP93xx	920T
Freescale	i.MX1/MXL	920T
Freescale	MAC71xx	7TDMI-S
NXP (Philips)	LPC21xx	7TDMI-S
NXP (Philips)	LPC22xx	7TDMI-S
NXP (Philips)	LPC23xx	7TDMI-S
NXP (Philips)	LPC24xx	7TDMI-S
NXP (Philips)	LPC288x	7TDMI-S
NXP (Philips)	LPC3180	926EJ-S
Samsung	KS32C50100	7TDMI
Sharp	LH754xx	7TDMI
Sharp	LH79520	7TDMI
Sharp	LH79524/5	7TDMI
Sharp	LH7A400	922T
Sharp	LH7A404	922T
STMicro	STR71x	7TDMI
STMicro	STR75x	7TDMI
STMicro	STR91x	966E-S
TI	TMS470	7TDMI

See www.smxrtos.com/processors for the latest information. Evaluation kits for these are available at www.smxrtos.com/eval.

Development Tools Supported

- IAR Embedded Workbench v5.11, v4.4x
- GNU C/C++

Development System Requirements

- Windows 9x, 2000, ME, NT 4, or XP
- JTAG interface such as Abatron BDI2000, EPI Majic, IAR J-Link/J-Trace, Lauterbach TRACE32, or Signum JTAGjet. For IAR, J-Link/J-Trace is recommended.

smxARM Development Kit Contents

- Pre-built smxARM kernel library
- Source code platform (Protosystem) for an easy start (configured for an eval board)
- smxBSP for the selected processor
- SMX Quick Start, smx Target Guide, smx User's Guide, and smx Reference Manual
- Site development license
- Royalty-free license for one developed product

smxBSP and Startup Code

The smxARM development kit includes smxBSP, startup code, and drivers for on-chip peripherals, such as timers and UART's. smxFS, smxNS,

smxUSB, etc. have drivers to support on-chip and external controllers. Also included is a project file for the IAR IDE to begin your application. If your processor is not in the table to the left, contact us — we are steadily adding new supported processors. For a non-supported processor, you can start with the closest smxBSP and adapt it. See the smxBSP brochure for more information. We recommend that you purchase the development board listed on our website for your processor. Use this to get a quick start, then modify smxBSP and the startup code for your custom board.

Easy Upgrade to/from Other Processors

smxARM shares the same code base with smx86, smxCF, and smxPPC. Therefore it is easy to migrate between smxARM and other processor versions of smx. If you have experience with smx on one processor, then you are already well down the learning curve for a new project using a different processor.

Debugger Support

smxARM supports symbolic debugging for the IAR debugger. smxARM also supports Lauterbach

Performance

max interrupt latency: 78 clocks (== 0.4 microsec on 200 MHz ARM)
task switch time: 8.5 microsec (measured on 200 MHz ARM922T (Sharp LH7A400))

RAM Usage

1. **smx global variables:** 575 bytes
2. **stack space:** num stacks * stack size (typical stack size is 500 to 1000 bytes)
3. **heap space:** space for control blocks + lsr queue + heap stacks (if any) + error buffer + event buffer + handle table

Notes

1. Space for control blocks depends on the number of smx objects used. Control blocks range from 12 to 76 bytes.
2. The lsr queue size is the number of lsr's that can be enqueued * 8 bytes per entry. Typically 20 to 100 elements.
3. The error buffer is optional. Its size is the number of entries * 12 bytes per entry.
4. The event buffer is optional. Its size is the number of entries * 24 bytes per entry.
5. The handle table is optional. Its size is the number of entries * 8 bytes per entry. The handle table is used only by smxAware, and smxDLM.

TRACE32 and Signum's JTAGjet + Chameleon debugger. smxAware supports the IAR C-SPY debugger. smxAware is a DLL that adds smx task-awareness to the debugger. With it, the debugger is aware of all tasks and smx objects running in the system, and you can:

- Display information about kernel specific objects such as tasks, lsrs, semaphores, exchanges, messages, events, heaps, stacks etc. from an entry added to the main menu.
- View errors, profiling, and other diagnostic information.
- View a graphical window that shows event timelines, CPU usage, and stack usage.
- Display a trace log created by simple string markers output by the code.
- Set task-specific breakpoints. The breakpoint will be triggered only if it is hit while the specified task is running. (Not all debuggers support this.)

Please refer to the smxAware brochure for further information. Also, the User's Guide is available on our web site.

